

FAST - [003411e:1]

File View Edit Tools Window Help

Drafts

- BRS: b
- Pending
- Active
 - L1: (1089970) (plac\$3 or placement or wir\$3 or rout\$3)and circuit
 - L2: (7860) ((tim\$3 near9 edge)or slack) same path
 - L3: (1771) ((tim\$3 near9 edge)or slack) same graph\$4
 - L4: (9566) ((tim\$3 near9 edge)or slack) same compar\$4
 - L5: (49) 11 and 12 and 13 and 14
 - L6: (316821) tim\$3 and edge and(priority or weight\$3)
 - L7: (28) 15 and 16
 - L8: (9) 17 and(bottleneck or bottle-neck)
- Failed
- Saved
- Favorites
- Tagged (0)

Browse Queue

DB: USPA1 ☒ Plots

Default operator: ☒ Highlight all hit terms initially

17 and(bottleneck or bottle-neck)

	U	I	Document ID	IP	Title	Current	Current XRef
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6415384 B1	21	Hardware/software co-synthesis of dynamically re	713/100	713/1
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6359861 B1	2	Method for scheduling transmissions in a buffere	370/230	370/397;
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6289488 B1	22	Hardware-software co-synthesis of hierarchical h	716/1	712/28;
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6230303 B1	23	Proximity-based cluster allocation for hardware-	716/7	716/18
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6178542 B1	22	Hardware-software co-synthesis of embedded syste	716/18	703/13;
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6112023 A	23	Scheduling-based hardware-software co-synthesis	703/27	703/28;
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6110220 A	21	Concurrent hardware-software co-synthesis of har	716/3	716/2;
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6097886 A	23	Cluster-based hardware-software co-synthesis of	703/23	712/28;
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6086628 A	23	Power-related hardware-software co-synthesis of	716/7	712/28;

✓ Hit Details HTML

Ready